

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	99568	(multiplexer)	USPAT	OR	ON	2009/01/22 19:51
S2	8176	(multiplexer\$1) AND (((multiple) or (plural \$5) or (several)) adj ((input\$1) or (channel \$1))) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND (((multiple) or (plural\$5) or (several)) adj ((output \$1) or (channel\$1)))	USPAT	OR	ON	2009/01/22 19:51
S3	1162	(multiplexer\$1) AND (((multiple) or (plural \$5) or (several)) adj ((input\$1) or (channel \$1))) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND (((multiple) or (plural\$5) or (several)) adj ((output \$1) or (channel\$1))) AND (((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB))	USPAT	OR	ON	2009/01/22 19:51

S4	585	(multiplexer\$1) AND (((multiple) or (plural \$5) or (several)) adj ((input\$1) or (channel \$1))) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND (((multiple) or (plural\$5) or (several)) adj ((output \$1) or (channel\$1))) AND (((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB)) AND (priorit\$3)	USPAT	OR	ON	2009/01/22 19:51
S5	254	(multiplexer\$1) AND (((multiple) or (plural \$5) or (several)) adj ((input\$1) or (channel \$1))) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND (((multiple) or (plural\$5) or (several)) adj ((output \$1) or (channel\$1))) AND (((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB)) AND (priorit\$3) AND (((direct) adj (memory) adj (access)) or (DMA))	USPAT	OR	ON	2009/01/22 19:51

S6	91	(multiplexer\$1) AND (((multiple) or (plural \$5) or (several)) adj ((input\$1) or (channel \$1))) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND (((multiple) or (plural\$5) or (several)) adj ((output \$1) or (channel\$1))) AND (((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB)) AND (priorit\$3) AND ((((direct) adj (memory) adj (access)) or (DMA)) near1 ((controller\$1) or (manager\$1) or (unit\$1)))	USPAT	OR	ON	2009/01/22 19:51
S7	14	(multiplexer\$1) AND (((multiple) or (plural \$5) or (several)) adj ((input\$1) or (channel \$1))) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND (((multiple) or (plural\$5) or (several)) adj ((output \$1) or (channel\$1))) AND (((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB)) AND (priorit\$3) AND ((((direct) adj (memory) adj (access)) or (DMA)) near1 ((controller\$1) or (manager\$1) or (unit\$1))) AND (((input) or (source) or (channel)) adj	USPAT	OR	ON	2009/01/22 19:51

		((identifier) or (identification) or (ID)))				
S8	25	(multiplexer\$1) AND (((multiple) or (plural \$5) or (several)) adj ((input\$1) or (channel \$1))) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND (((multiple) or (plural\$5) or (several)) adj ((output \$1) or (channel\$1))) AND (((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB)) AND (priorit\$3) AND ((((direct) adj (memory) adj (access)) or (DMA)) near1 ((controller\$1) or (manager\$1) or (unit\$1))) AND (((input) or (source) or (channel)) adj2 ((identifier) or (identification) or (ID)))	USPAT	OR	ON	2009/01/22 19:51
S9	13	(multiplexer\$1) AND (((multiple) or (plural \$5) or (several)) adj ((input\$1) or (channel \$1))) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND (((multiple) or (plural\$5) or (several)) adj ((output \$1) or (channel\$1))) AND (((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB)) AND	USPAT	OR	ON	2009/01/22 19:51

		(priorit\$3) AND ((((direct) adj (memory) adj (access)) or (DMA)) near1 ((controller\$1) or (manager\$1) or (unit\$1))) AND (((input) or (source) or (channel)) adj2 ((identifier) or (identification) or (ID))) AND ((output) adj ((memor\$4) or (FIFO \$1) or (buffer\$1) or (queue\$1) or (register \$1)))				
S10	16	(multiplexer\$1) AND (((multiple) or (plural \$5) or (several)) adj ((input\$1) or (channel \$1))) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND (((multiple) or (plural\$5) or (several)) adj ((output \$1) or (channel\$1))) AND (((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB)) AND (priorit\$3) AND ((((direct) adj (memory) adj (access)) or (DMA)) near1 ((controller\$1) or (manager\$1) or (unit\$1))) AND (((input) or (source) or (channel)) adj2 ((identifier) or (identification) or (ID))) AND ((output) near2 ((memor\$4) or (FIFO \$1) or (buffer\$1) or (queue\$1) or (register \$1)))	USPAT	OR	ON	2009/01/22 19:51

S11	25	(multiplexer\$1) AND (((multiple) or (plural \$5) or (several)) adj ((input\$1) or (channel \$1))) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND (((multiple) or (plural\$5) or (several)) adj ((output \$1) or (channel\$1))) AND (((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB)) AND (priorit\$3) AND ((((direct) adj (memory) adj (access)) or (DMA)) near1 ((controller\$1) or (manager\$1) or (unit\$1))) AND (((input) or (source) or (channel)) adj2 ((identifier) or (identification) or (ID)))	USPAT	OR	ON	2009/01/22 19:51
S12	112	(multiplexer\$1) AND (((multiple) or (multi) or (plural\$5) or (several)) adj ((input\$1) or (channel \$1))) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND (((multiple) or (multi) or (plural\$5) or (several)) adj ((output\$1) or (channel\$1))) AND (((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB)) AND (priorit\$3) AND ((((direct) adj	USPAT	OR	ON	2009/01/22 19:51

		(memory) adj (access)) or (DMA)) near1 ((controller\$1) or (manager\$1) or (unit\$1)) or (DMAC))				
S13	46	(multiplexer\$1) AND (((multiple) or (multi) or (plural\$5) or (several)) adj ((input\$1) or (channel \$1))) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND (((multiple) or (multi) or (plural\$5) or (several)) adj ((output\$1) or (channel\$1))) AND (((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB)) AND (priorit\$3) AND ((((direct) adj (memory) adj (access)) or (DMA)) near1 ((controller\$1) or (manager\$1) or (unit\$1)) or (DMAC)) AND (((input) or (source) or (channel)) near2 ((identifier) or (identification) or (ID)))	USPAT	OR	ON	2009/01/22 19:51
S14	45	(multiplexer\$1) AND (((multiple) or (multi) or (plural\$5) or (several)) adj ((input\$1) or (channel \$1))) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND (((multiple) or (multi) or (plural\$5) or (several)) adj ((output\$1) or	USPAT	OR	ON	2009/01/22 19:51

		(channel\$1))) AND (((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB)) AND (priorit\$3) AND ((((direct) adj (memory) adj (access)) or (DMA)) near1 ((controller\$1) or (manager\$1) or (unit\$1)) or (DMAC)) AND (((input) or (source) or (channel)) near2 ((identifier) or (identification) or (ID)))AND (((output) or (transmit\$4)) near2 ((memor\$4) or (FIFO \$1) or (buffer\$1) or (queue\$1) or (register \$1)))				
S15	45	(multiplexer\$1) AND (((multiple) or (multi) or (plural\$5) or (several)) adj ((input\$1) or (channel \$1))) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND (((multiple) or (multi) or (plural\$5) or (several)) adj ((output\$1) or (channel\$1))) AND (((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB)) AND (priorit\$3) AND ((((direct) adj (memory) adj (access)) or (DMA)) near1 ((controller\$1) or (manager\$1) or	USPAT	OR	ON	2009/01/22 19:51

		(unit\$1)) or (DMAC)) AND (((input) or (source) or (channel)) near2 ((identifier) or (identification) or (ID)))AND (((output) or (transmit\$4) or (transmission)) near2 ((memor\$4) or (FIFO \$1) or (buffer\$1) or (queue\$1) or (register \$1)))				
S16	45	(multiplexer\$1) AND (((multiple) or (multi) or (plural\$5) or (several)) adj ((input\$1) or (channel \$1))) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND (((multiple) or (multi) or (plural\$5) or (several)) adj ((output\$1) or (channel\$1))) AND (((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB)) AND (priorit\$3) AND ((((direct) adj (memory) adj (access)) or (DMA)) near1 ((controller\$1) or (manager\$1) or (unit\$1)) or (DMAC)) AND (((input) or (source) or (channel)) near2 ((identifier) or (identification) or (ID)))AND (((output) or (transmit\$4) or (transmission)) near2 ((memor\$4) or (FIFO \$1) or (buffer\$1) or	USPAT	OR	ON	2009/01/22 19:51

		(queue\$1) or (register \$1)))				
S17	2075	((370/535) or (370/536) or (370/537) or (370/539) or (370/540) or (370/541) or (370/542) or (370/544)).CCLS.	USPAT; USOCR	OR	OFF	2009/01/22 19:51
S18	979	(370/535).CCLS.	USPAT; USOCR	OR	OFF	2009/01/22 19:51
S19	278	(370/536).CCLS.	USPAT; USOCR	OR	OFF	2009/01/22 19:51
S20	660	(370/537).CCLS.	USPAT; USOCR	OR	OFF	2009/01/22 19:51
S21	143	(370/539).CCLS.	USPAT; USOCR	OR	OFF	2009/01/22 19:51
S22	117	(370/540).CCLS.	USPAT; USOCR	OR	OFF	2009/01/22 19:51
S23	75	(370/541).CCLS.	USPAT; USOCR	OR	OFF	2009/01/22 19:51
S24	157	(370/542).CCLS.	USPAT; USOCR	OR	OFF	2009/01/22 19:51
S25	43	(370/544).CCLS.	USPAT; USOCR	OR	OFF	2009/01/22 19:51
S26	8887	(multiplexer\$1) AND ((input\$1) or (channel \$1)) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND (((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB))	USPAT	OR	ON	2009/01/30 14:46

S27	5358	((multiplexer\$1) SAME ((input\$1) or (channel\$1)) SAME ((memor\$4) or (FIFO \$1) or (buffer\$1) or (queue\$1) or (register \$1))) AND ((((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB)))	USPAT	OR	ON	2009/01/30 14:47
S28	232	((multiplexer\$1) SAME ((input\$1) or (channel\$1)) SAME ((memor\$4) or (FIFO \$1) or (buffer\$1) or (queue\$1) or (register \$1)) SAME (priorit \$3)) AND ((((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB)))	USPAT	OR	ON	2009/01/30 14:47
S29	57	((multiplexer\$1) SAME ((input\$1) or (channel\$1)) SAME ((memor\$4) or (FIFO \$1) or (buffer\$1) or (queue\$1) or (register \$1)) SAME (priorit \$3)) AND ((((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB))) AND (((direct) adj (memory) adj (access)) or (DMA))	USPAT	OR	ON	2009/01/30 14:50

S30	124	((multiplex\$3) SAME ((input\$1) or (channel\$1)) SAME ((memor\$4) or (FIFO\$1) or (buffer\$1) or (queue\$1) or (register\$1)) SAME (priorit\$3)) AND ((((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB))) AND (((direct) adj (memory) adj (access)) or (DMA))	USPAT	OR	ON	2009/02/04 12:19
S31	57	((multiplexer\$1) SAME ((input\$1) or (channel\$1)) SAME ((memor\$4) or (FIFO\$1) or (buffer\$1) or (queue\$1) or (register\$1)) SAME (priorit\$3)) AND ((((least) adj (significant) adj (bit)) or (LSB)) AND (((most) adj (significant) adj (bit)) or (MSB))) AND (((direct) adj (memory) adj (access)) or (DMA))	USPAT	OR	ON	2009/02/04 12:20
S32	67	S30 NOT S31	USPAT	OR	ON	2009/02/04 12:20

2/ 4/ 2009 7:25:51 PM

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